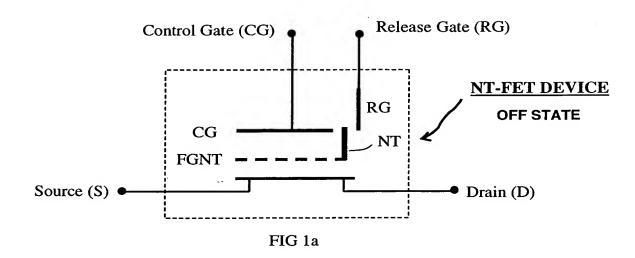
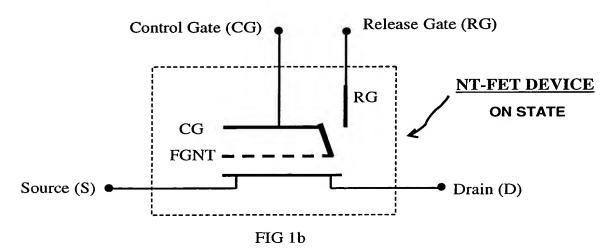


REPLACEMENT SHEET
Appl. No. 10/811,191
Filing Date: March 26, 2004
Inventor: Thomas Rueckes et al.





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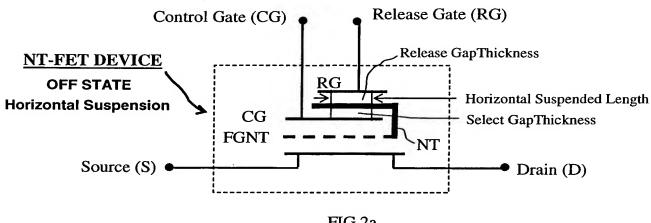
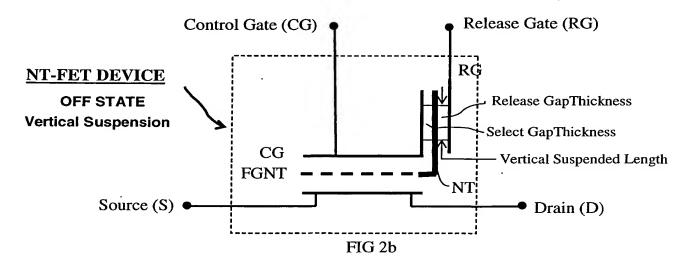


FIG 2a



REPLACEMENT SHEET-Appl. No. 10/811,191 Filing Date: March 26, 2004 Inventor: Thomas Rueckes et al.

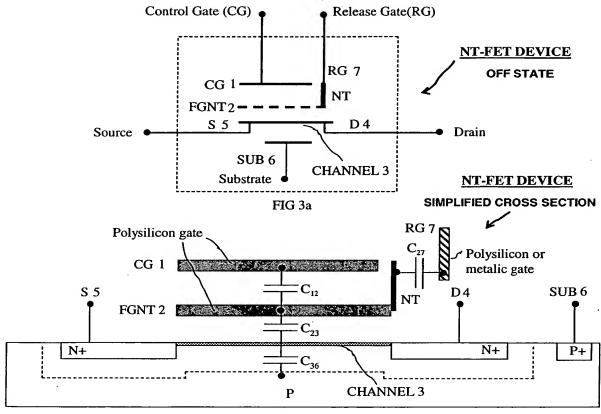
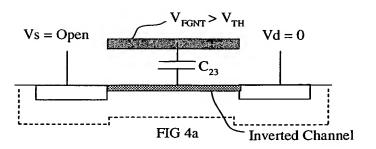


FIG 3b

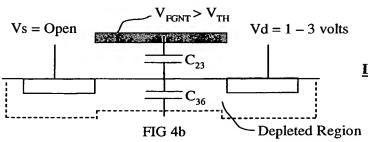
# REPLACEMENT SHEET Appl. No. 10/811,191 Filing Date: March 26, 2004 Inventor: Thomas Rueckes et al.

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### **CHANNEL REGION MODULATION**



Inverted Channel: electrons at V = 0 volts

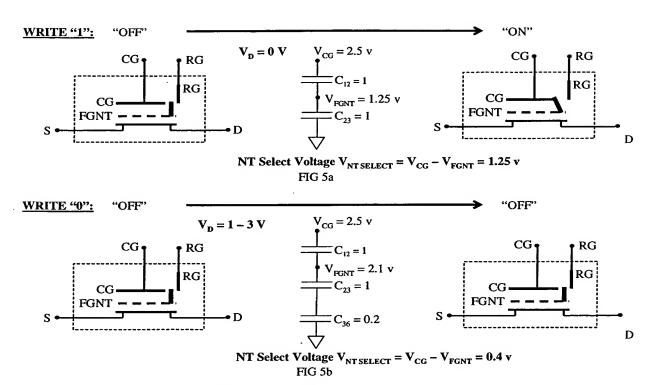


**Depleted Channel**: electrons flow to positive terminal

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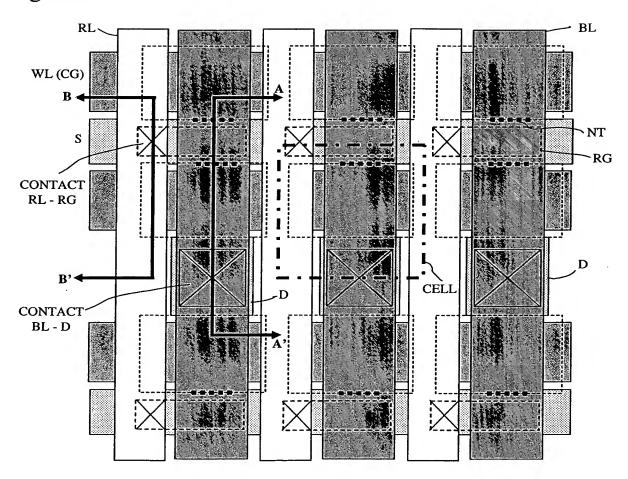
Inventor: Thomas Rueckes et al.

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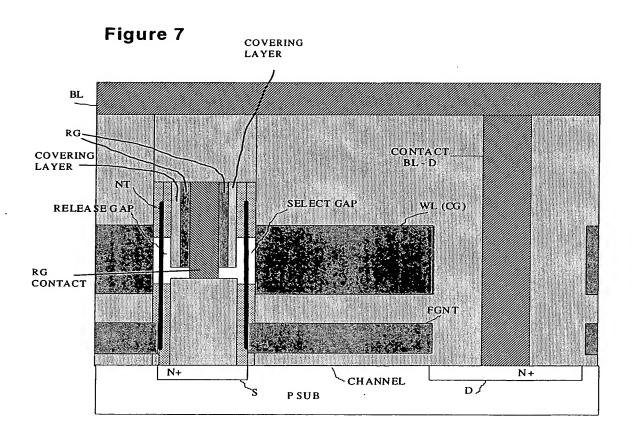


NET: SELECT VOLTAGE THRESHOLD RANGE > 0.4 v & < 1.25 v

Figure 6



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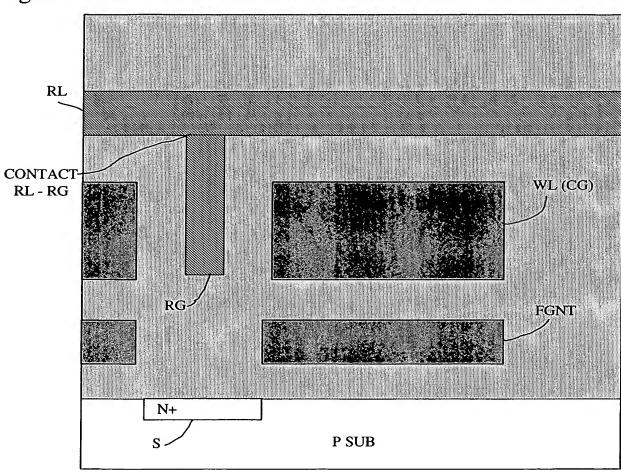
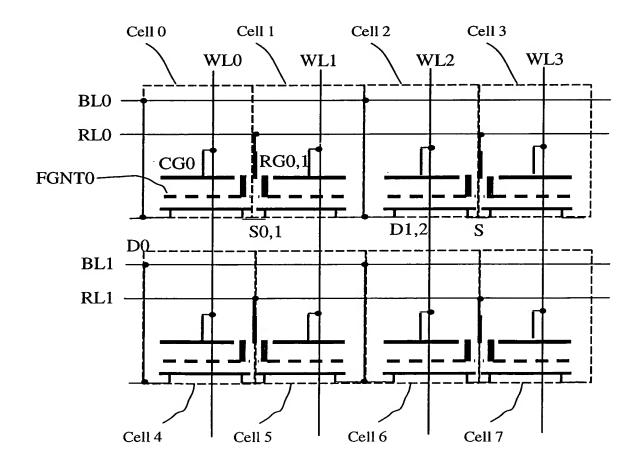


Figure 9



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Figure 10

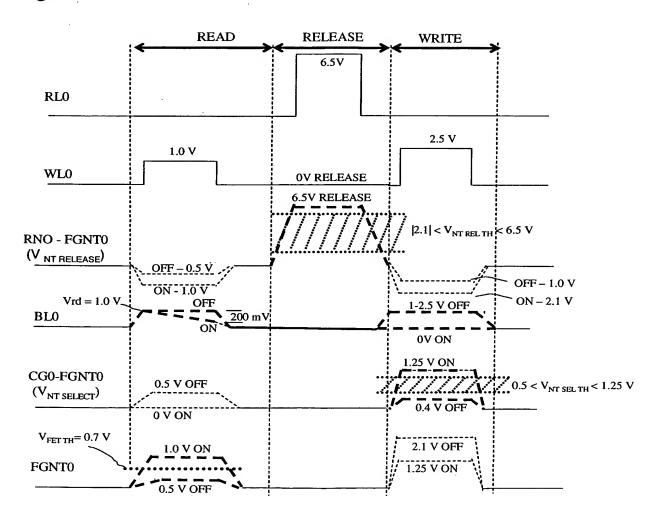


Figure 11

